

FIG. 1

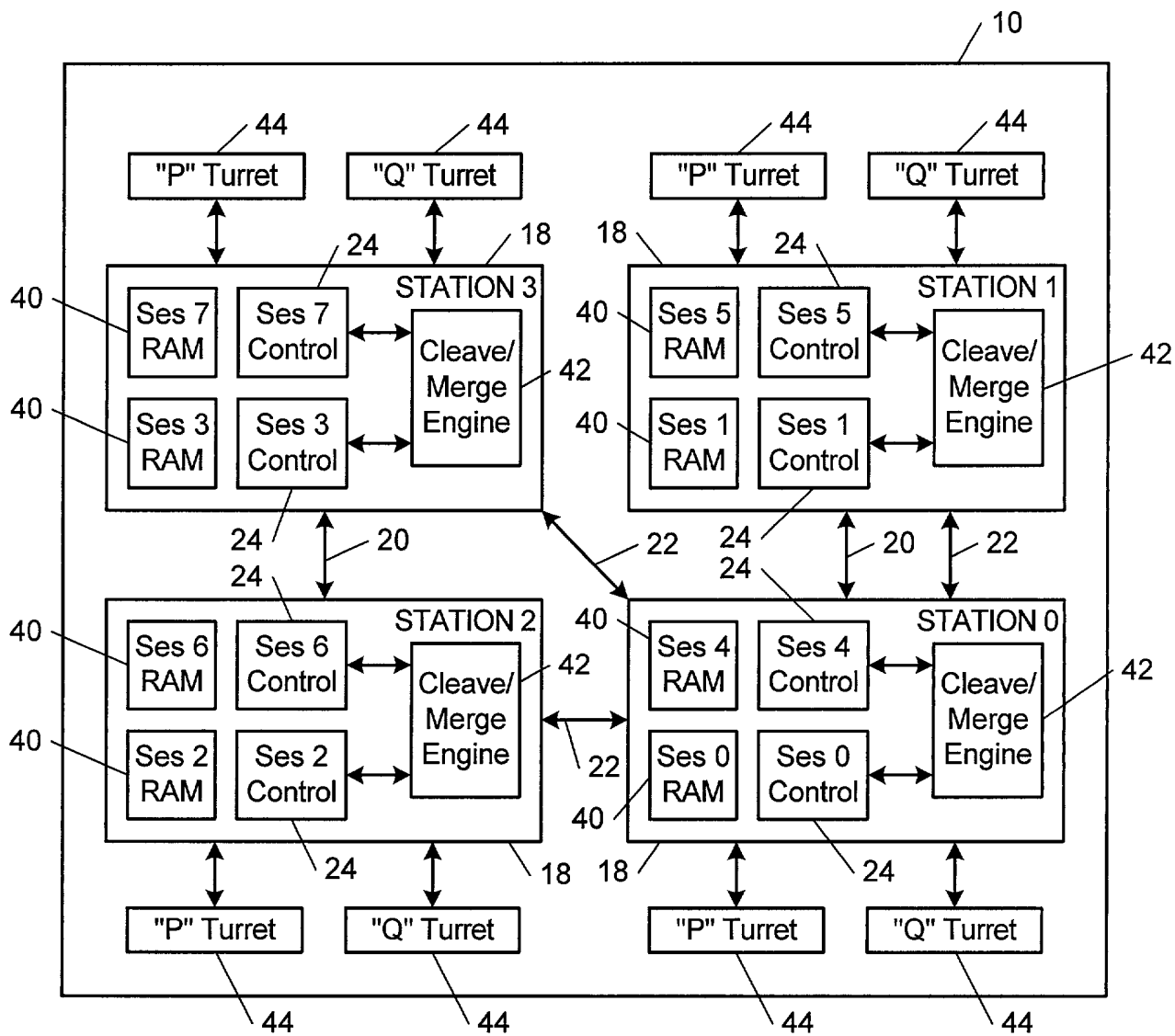


FIG. 2

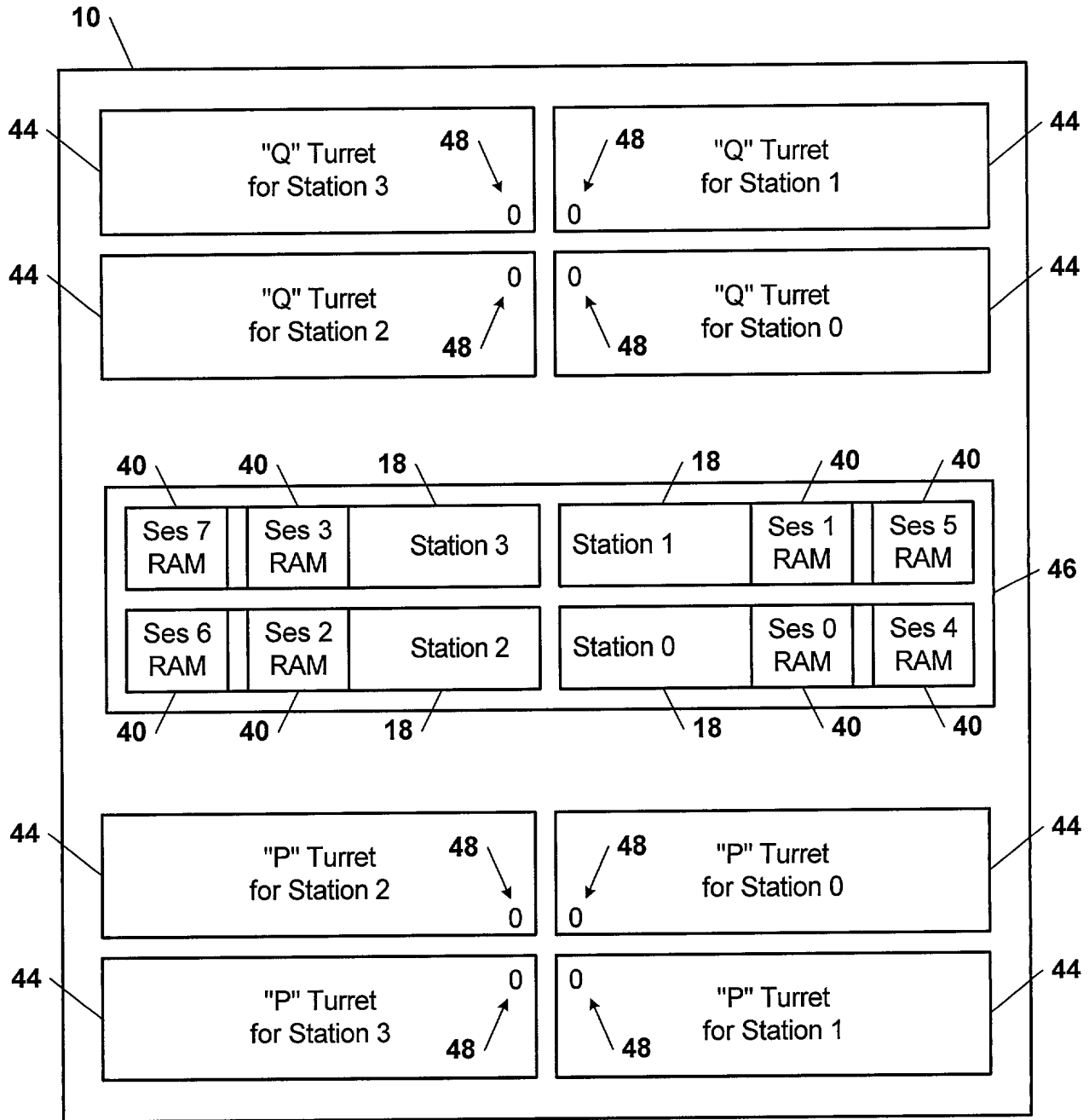


FIG. 3

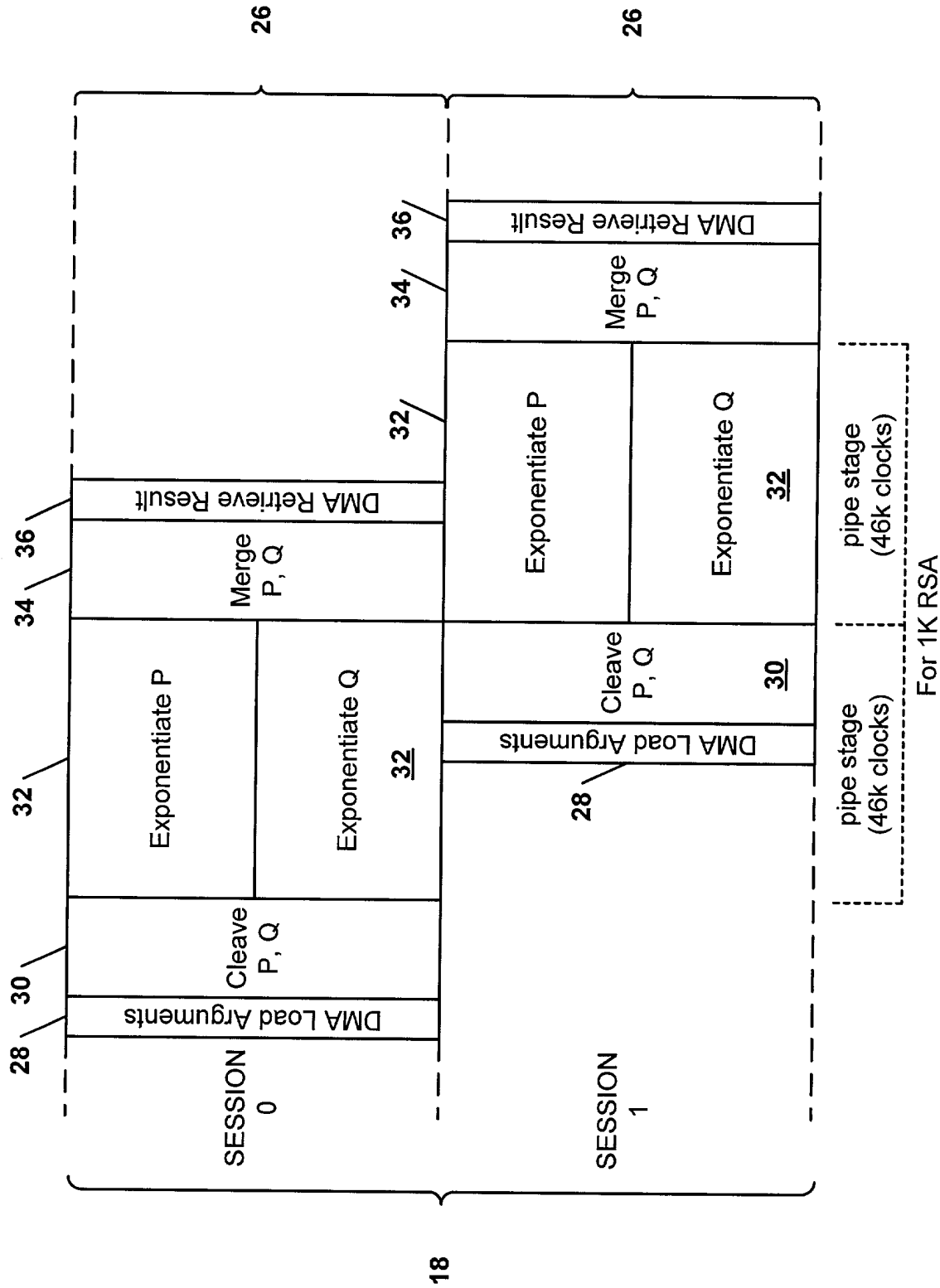
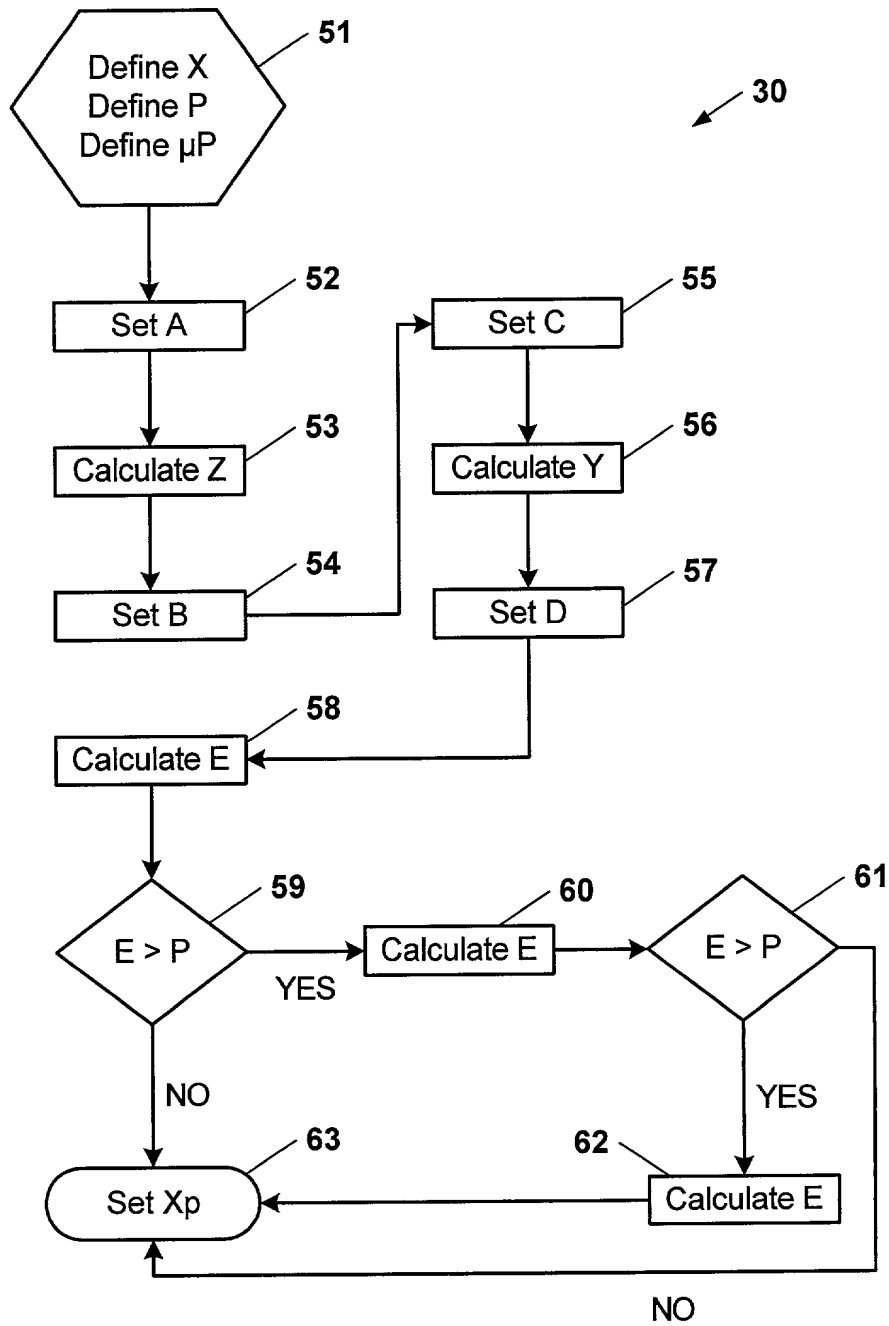
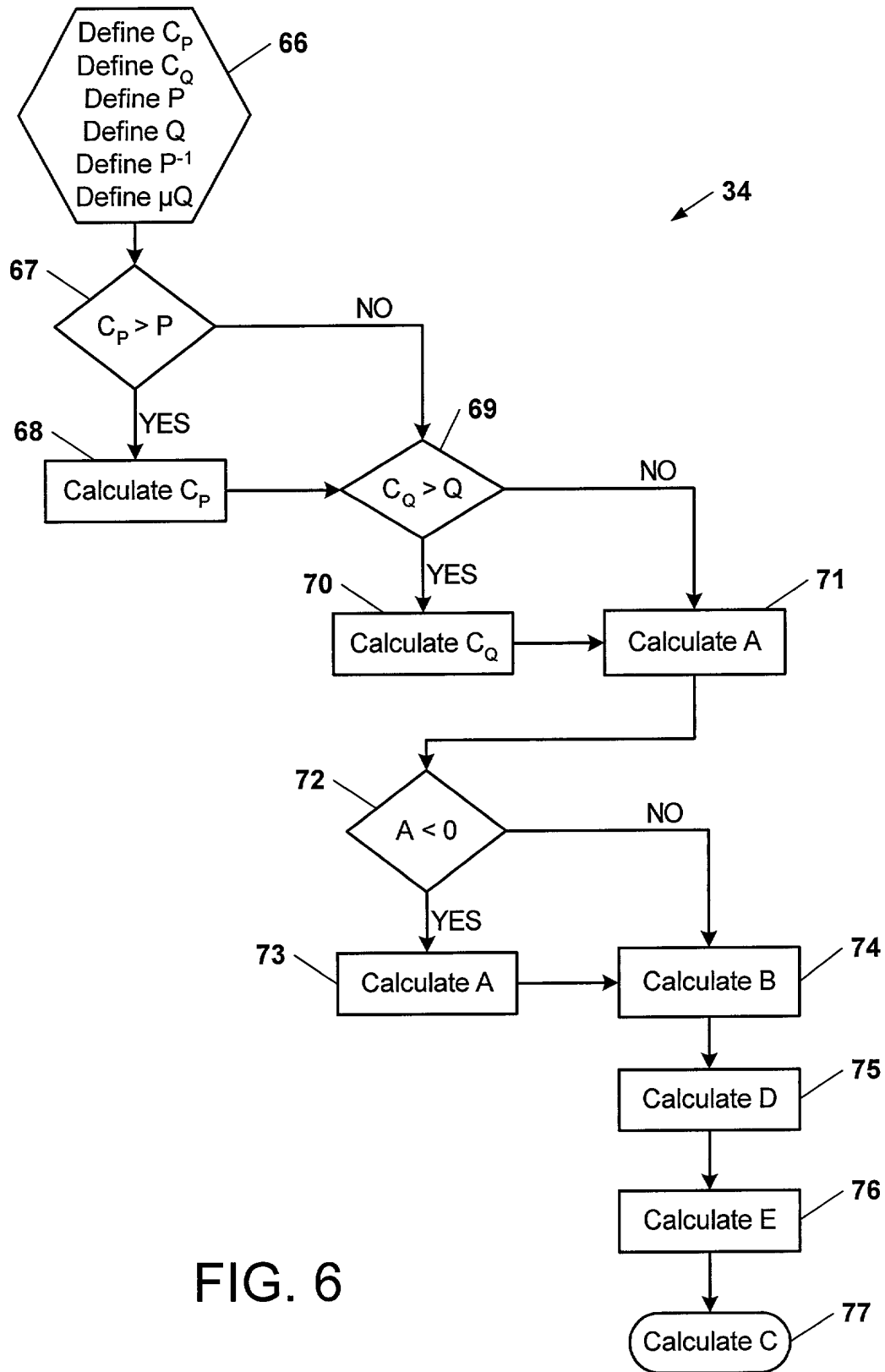


FIG. 4





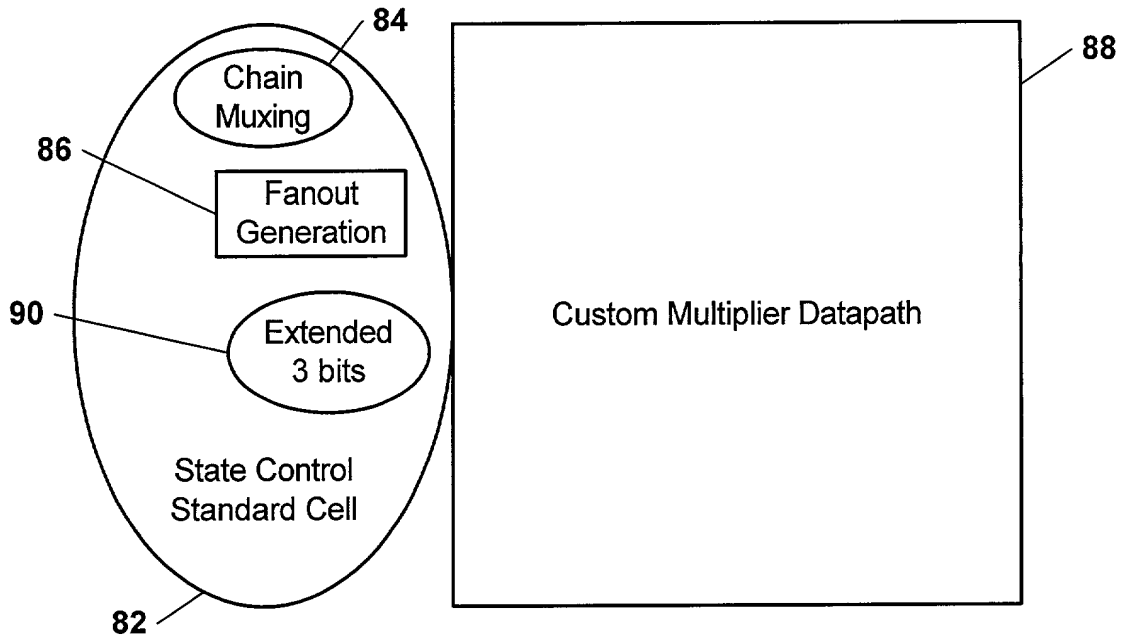


FIG. 7

Gated Clock, Fanout, and X drivers	0	63	94
	Dual 128 bit full adders	<u>92</u>	94
	127	64	94
	511	448	94
	Dual 128 bit full adders	<u>92</u>	94
	384	447	94
	128	191	94
	Dual 128 bit full adders	<u>92</u>	94
	255	192	94
	383	320	94
	Dual 128 bit full adders	<u>92</u>	94
	256	319	94

88

FIG. 8

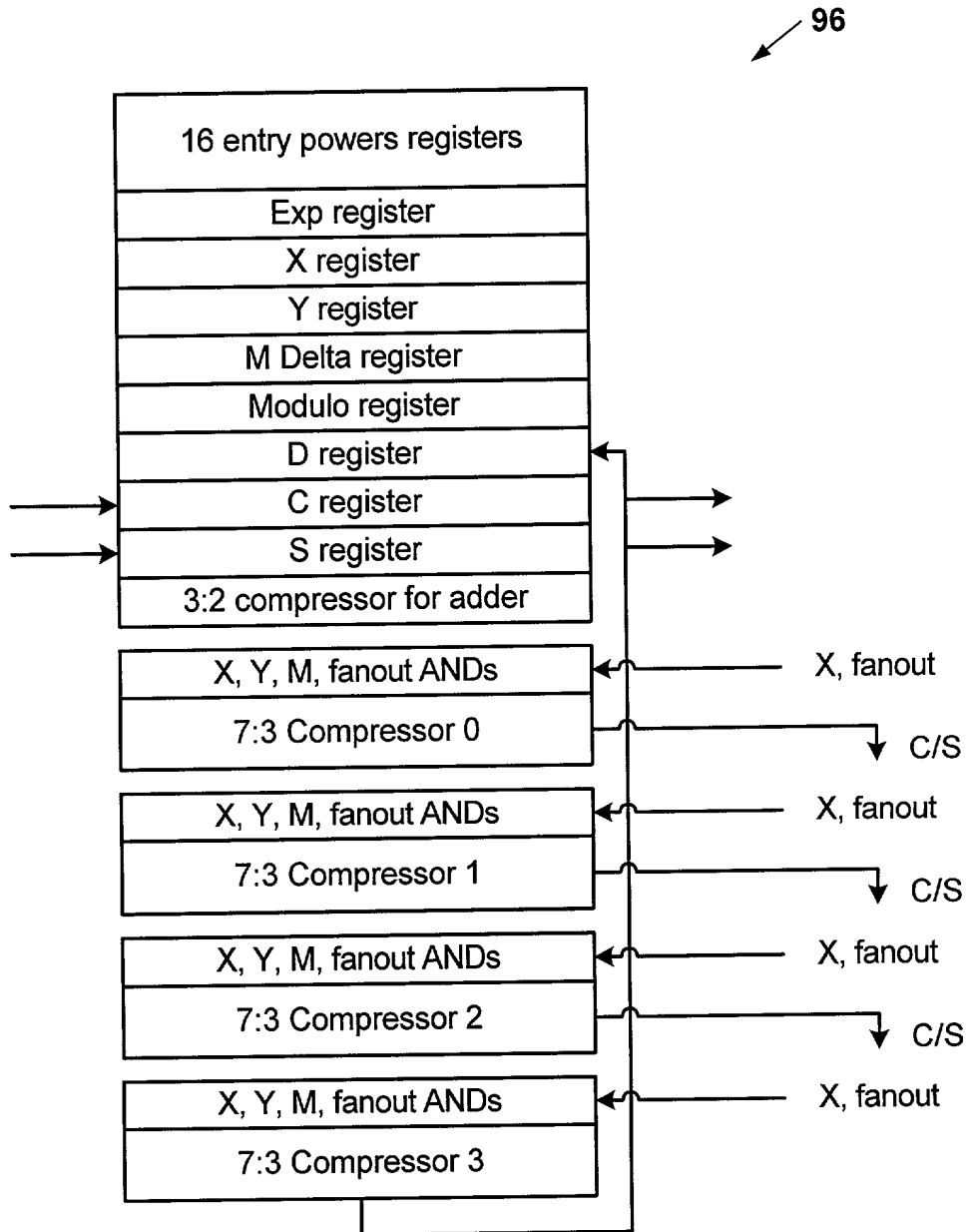


FIG. 9

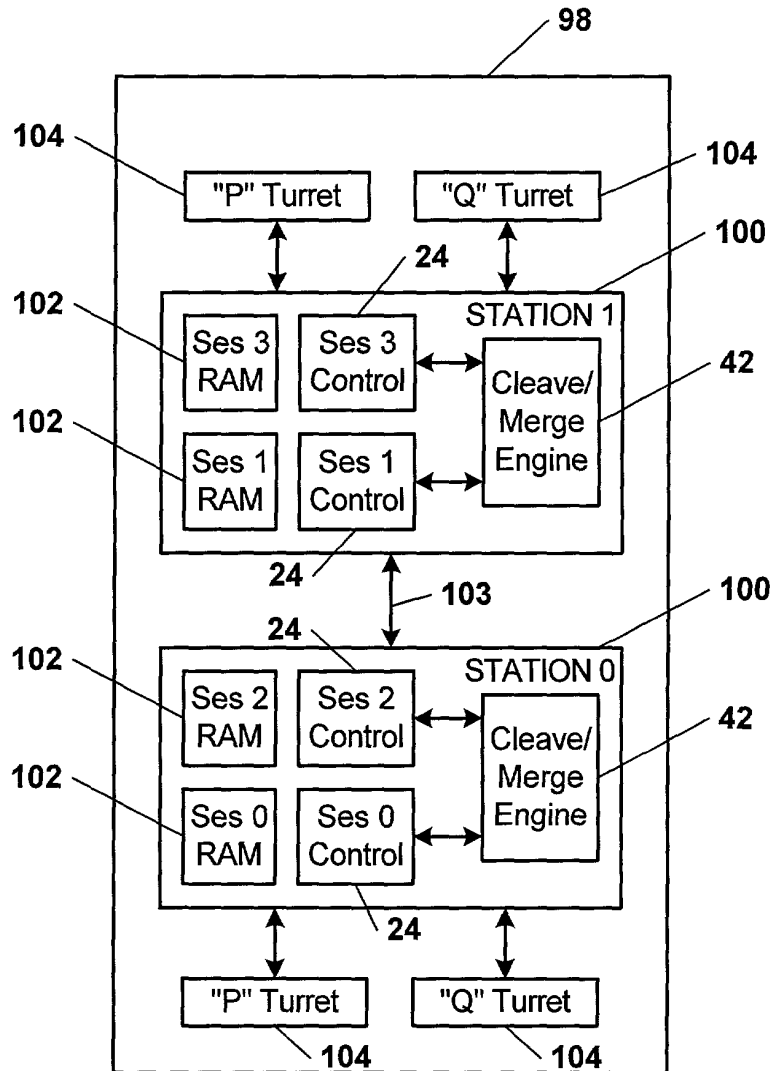


FIG. 10

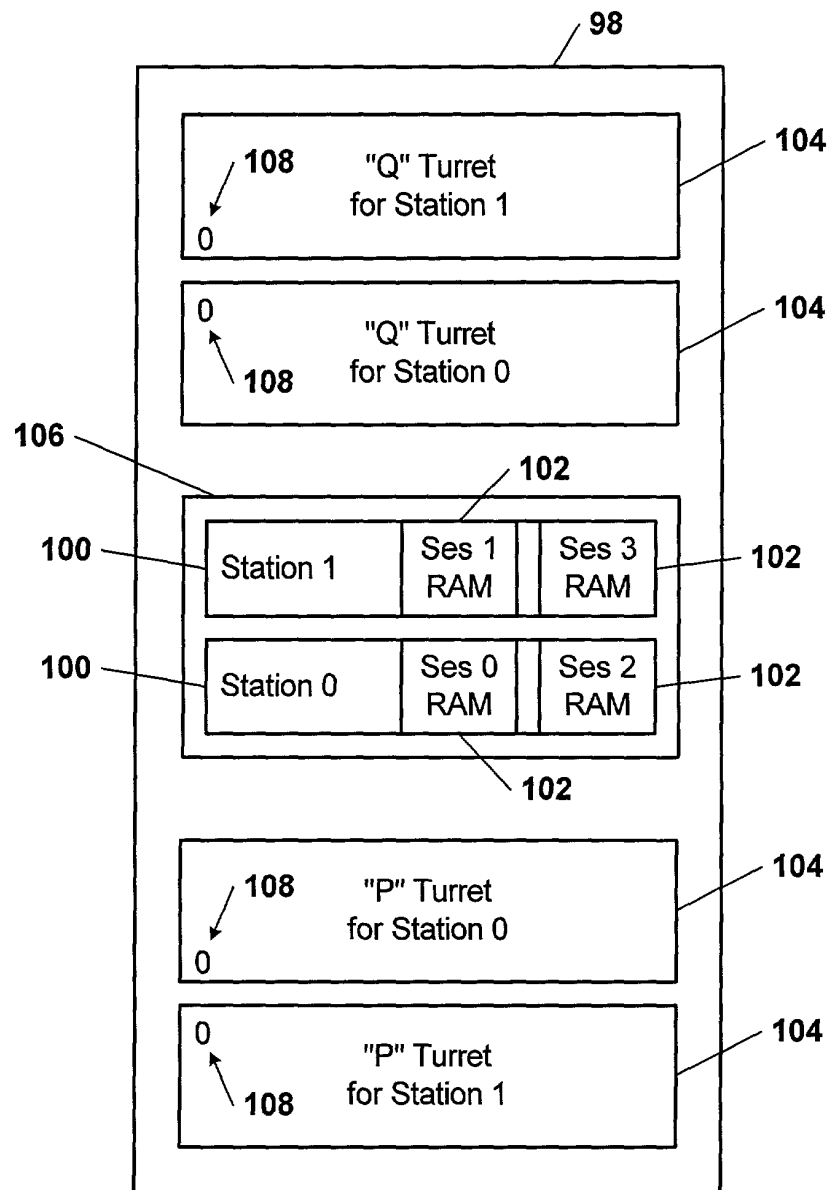


FIG. 11

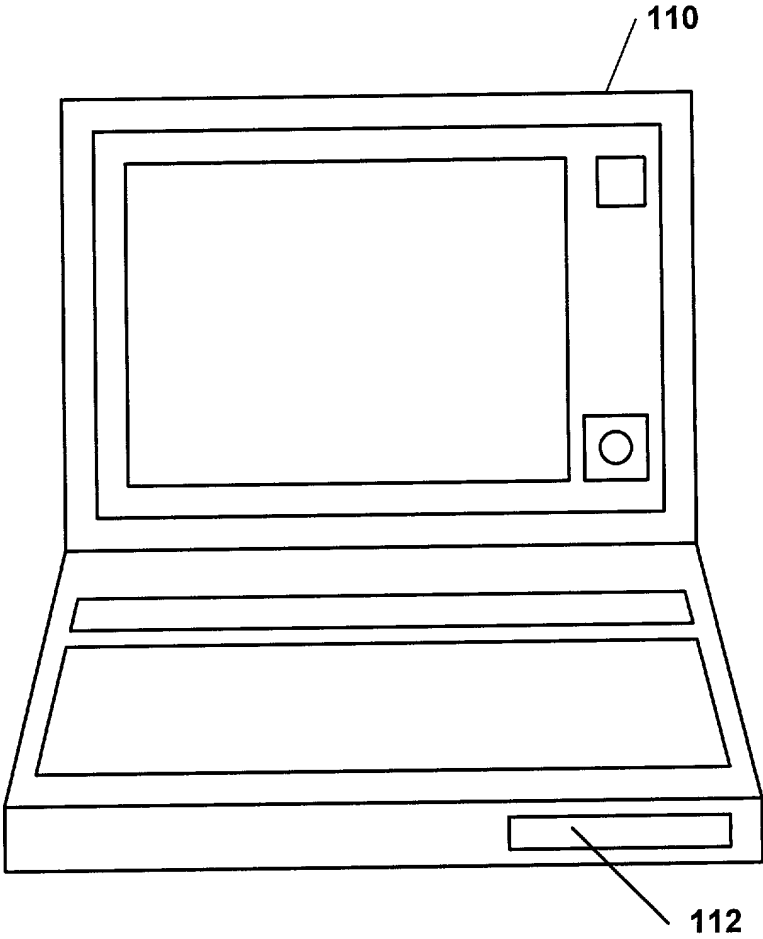


FIG. 12

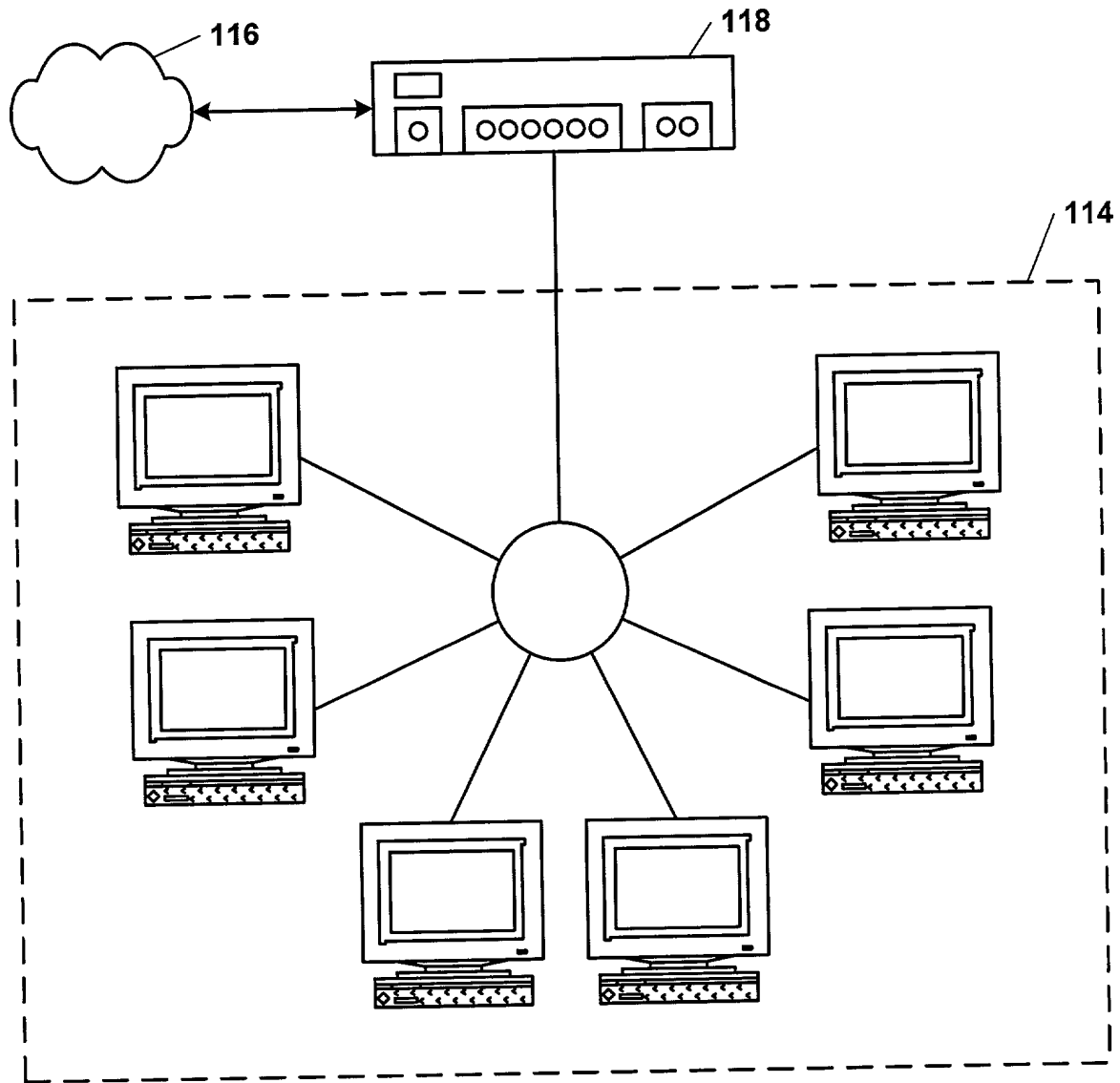


FIG. 13

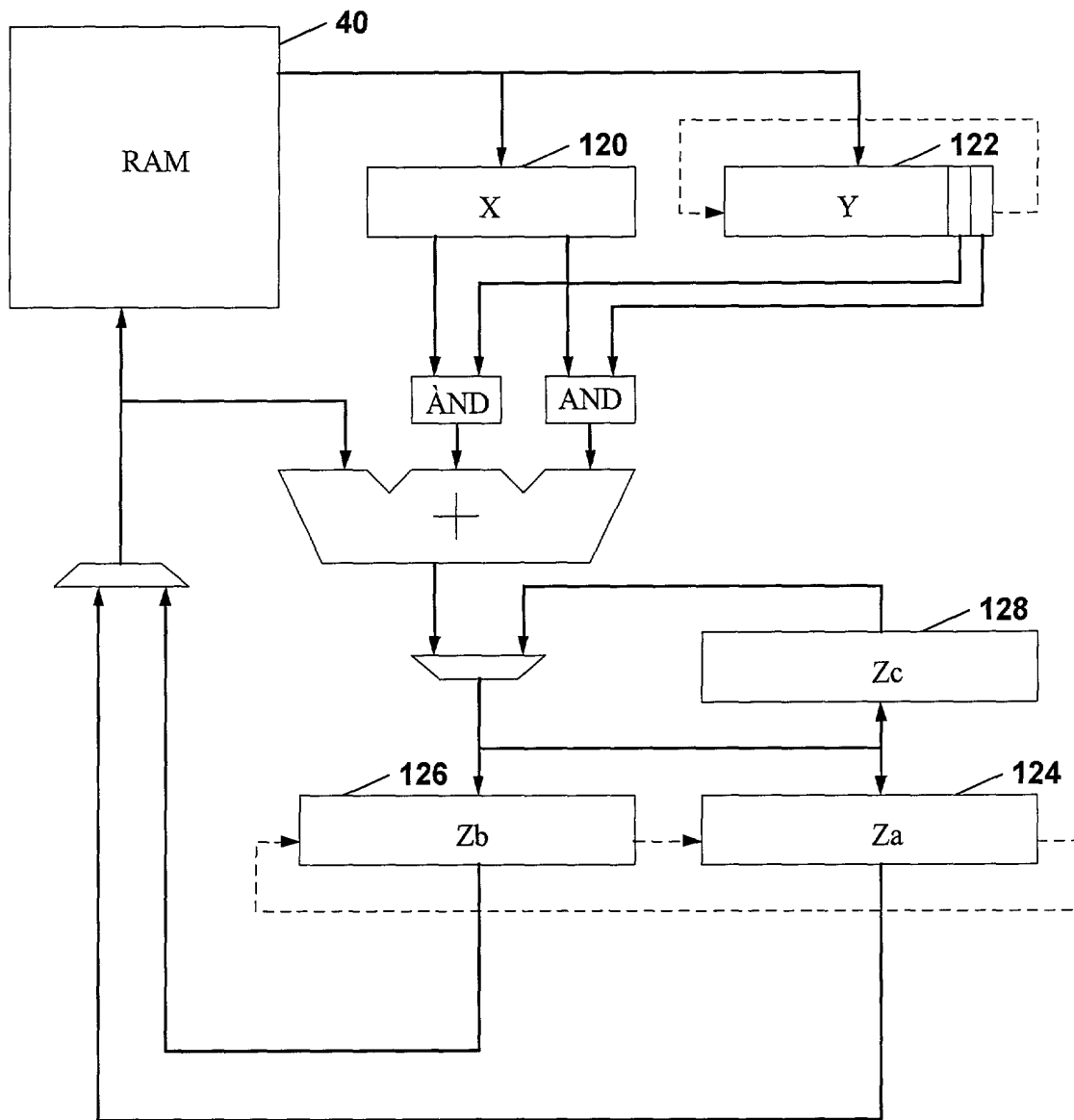


FIG. 14